

LESSON PLAN

Period	Date (Tentative)	Topic	Unit No.	Teaching Methodology	Remarks	Corrective Action Upon Review
1	26/02/18	Low power design overview: Introduction	I	Black Board		
2	27/02/18	Low voltage low power design		"		
3	01/03/18	Limitations		"		
	02/03/18					
4	05/03/18	Silicon on Insulator		"		
5	06/03/18	MOS/BiCMOS processes: Realization of BiCMOS		"		
6	08/03/18	processes -		"		
7	09/03/18	low cost medium speed digital		"		
8	12/03/18	Highest high performance digital		"		
9	13/03/18	Analog/Digital Integrated BiCMOS		"		
10	15/03/18	"				
11	16/03/18	Isolation in BiCMOS	II	BD		
12	19/03/18	Isolation techniques in MOS-fabric		BD		
13	20/03/18	Shallow and Deep trench		BD		
14	22/03/18	Advanced Isolation techniques -		BD		
15	23/03/18	Dielectric Isolation (DI)		BD		
16	24/03/18	Wafer bonding.		BD		
17	27/03/18	Smart-cut process		BD		
18	29/03/18	Deep Submicron process		BD		
19	30/03/18	key process steps.		BD		

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20	03/04/18	SoI CMOS		BS		
21	06/04/18	Internal DTT on SoI		BS		
22	9/04/18	Future trends and directions of		BS		
23	10/04/18	CMOS / BiCMOS processes		BS		
24	12/04/18	Device modeling: MOSFET Spice models	III	BS		
25	13/04/18	Advanced MOSFET models		BS		
26	16/04/18	Limitations of MOSFET models		BS		
27	17/04/18	Bipolar models: Ebers Moll model		BS		
28	19/04/18	Current source model		BS		
29	20/04/18	Hicup model		BS		
30	23-28/04/18	I MID Examinations				
31	30/04/18	Analytical and exp. characterization of subthreshold MOSFETs	IV	BS		
32	01/05/18	"				
33	04/05/18	"				
34	04/05/18	MOSFET in hybrid model environment				
35	05/05/18	"				
36	07/05/18	"				
37	08/06/18	CMOS and BiCMOS logic gates	V	BS		

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38	11/06/18	Conventional CMOS logic gates		blackboard		
39	12/06/18	Bicmos logic gates.		"		
40	14/06/18	Performance Evaluation.		"		
41	15/06/18	low-voltage (low power) logic circuits.	V	"		
42	18/06/18	operation		"		
43	19/06/18	Performance and competitive		"		
44	20/06/18	Evaluation of Advanced Bicmos Digital		"		
45	22/06/18	Circuits: Full-swing		"		
46	25/06/18	Multi drain (multi collector) bicmos		"		
47	26/06/18	Quasi-cmos		"		
48	28/06/18	Feedback type Bicmos		"		
49	29/06/18	ESD free B ² cmos.		"		
50	02/07/18	"		"		
51	03/07/18	low power latches and flipflops.	VI	Blackboard		
52	05/07/18	Evaluation of latches		PPT		
53	06/07/18	"		BS		
54	09/07/18	Flip flops		PPT		
55	11/07/18	"		BS		
56	13/07/18	quality measures		BS		
57	14/07/18	SR latch as		BS		

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58	16/07/18.	anchors of Flipflops		Blackboard		
59	17/07/18.	"		PPT		
60	19/07/18.	Design perspective		PPT		
61	20/07/18.	"		PPT		
	23/07/18 - 28/07/18	MID II				